IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended) A liquid crystal display comprising a TFT array substrate, a counter substrate facing the TFT substrate, and a liquid crystal interposed between the substrates, whereincomprising:

the TFT array substrate has a display area including a pixel electrode, and a terminal forming area,

the display area is provided with a pixel electrode, a switching element connected to the pixel electrode,

a gate line connected to the switching element and a source line connected to the switching element, and

thea terminal formed outside of said display area forming area is provided with a terminal electrode for connecting the gate line or and the source line to wirings from an at least one external signal source, wherein the terminal comprises a terminal electrode connected to said wirings from said external signal source, and forming area is located between the display area and the side where the TFT array substrate is cut off during fabrication of said liquid crystal display, and wherein a first metallic line and a second metallic line arranged beneath the terminal electrode, and both-connected to the terminal electrode via respective a contact holeshole, are arranged below the terminal electrode at the terminal forming area, and an insulating layer is interposed between the first metallic line and the second metallic line and serves, during fabrication of said liquid crystal display, to minimize exfoliation of the second metallic line, and short circuits resulting from such exfoliation, and

wherein the first metallic line and the second metallic line are made from different metallic layers in a first side of the terminal forming area facing is formed in a side of the display area, and the second metallic line is formed in a second side of the terminal forming area facing the side where the TFT array substrate is cut off or chamfered off; during fabrication of said liquid crystal display, the first metallic line below the terminal electrode is the source line which is made from the metal film for the source electrode and the drain electrode, and the second metallic line below the terminal electrode is the supplementary line which is made from the metal film for the gate line

wherein the TFT array substrate further comprises an insulating layer which is interposed between the first metallic line and the second metallic line;

wherein the first metallic line is formed over the insulating layer;
wherein said second metallic line is formed beneath the insulating layer; and
wherein any one of said first metallic line and said second metallic line is formed in the
same layer of said source line, and the other one of said first and second metallic lines is formed
in the same layer of said gate line.

2-6. (CANCELLED)

- 7. (New) The TFT array substrate of claim 1, wherein said first metallic line is connected with said source line.
- 8. (New) The TFT array substrate of claim 1, wherein said second metallic line is connected with said gate line.
- 9. (New) A display device comprising the TFT array substrate of claim 1.
- 10. (New) A display device comprising the TFT array substrate of claim 7.
- 11. (New) A display device comprising the TFT array substrate of claim 8.